ACKNOWLEDGEMENT RECEIPT

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Title of Invention	STRUCTURE AND METHOD OF FORMING A BIPOLAR TRANSISTOR HAVING A VOID BETWEEN EMITTER AND EXTRINSIC BASE

Submision Type : Utility Patent Filing

Application Number:

10/708563

EFS ID: 56949

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fucntion call.

First Named Applicant: Rama Divakaruni

Attorney Docket Number: FIS920030414US1

Timestamp: 2004-03-11 14:47:47 EDT

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